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## STATEMENT BY APPLICANT

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Application Number	10/770,435	
Filing Date	February 3, 2004	
First Named Inventor	Shi	
Art Unit	2816	*
Examiner Name	Luu, A.	_
Attorney Docket Number	RAMB-01015US1	

U. S. PATENT DOCUMENTS							
Examiner Initials*	Cite No. <sup>1</sup>	Document Number  Number-Kind Code <sup>2 (if known)</sup>	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear		
······································		<sup>US-</sup> 6504438	01-07-2003	Chang et al.			
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		us-6928128 B1	08-09-2005	Sidiropoulos			
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FOREIGN PATENT DOCUMENTS									
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document  Country Code <sup>3</sup> 'Number <sup>4</sup> 'Kind Code <sup>5</sup> ( <i>if known</i> )	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear				
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Substitute for form 1449B/PTO					1311	Complete if Known
					Application Number	10/770,435
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					Art Unit	2816
		(Use as many sheets as	necessary	)	Examiner Name	Luu, A.
	Sheet	2	of	2	Attorney Docket Number	RAMB-01015US1

NON PATENT LITERATURE DOCUMENTS						
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²			
		CHANG et al., "A 0.4-4Gb/s CMOS Quad Transceiver Cell using On-chip Regulated Dual-Loop PLLS," 2002 Symposium on VLSI Circuits Digest of Technical Papers				
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